FIG. 1

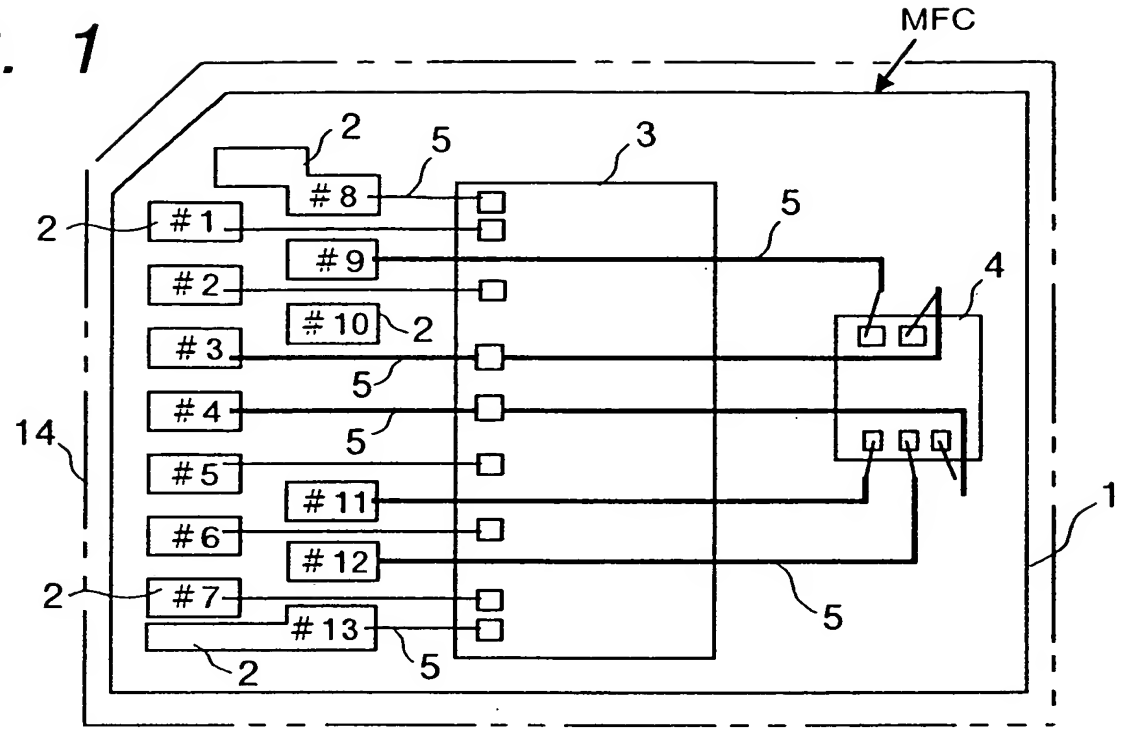


FIG. 2

pin #	7pin (1bit)	9pin (4bit)	13pin
1	(CS) NC	Data3	Data3
2	(Din) CMD	CMD	CMD
3	Vss1	Vss1	Vss1
4	Vdd	Vdd	Vdd
5	CLK	CLK	CLK
6	Vss2	Vss2	Vss2
7	(Dout) Data	Data0	Data0
8	-	Data2	Data2
9	-	-	I/O
10	-	-	NC
11	-	-	CLK
12	-	-	RST
13	-	Data1	Data1

FIG. 3

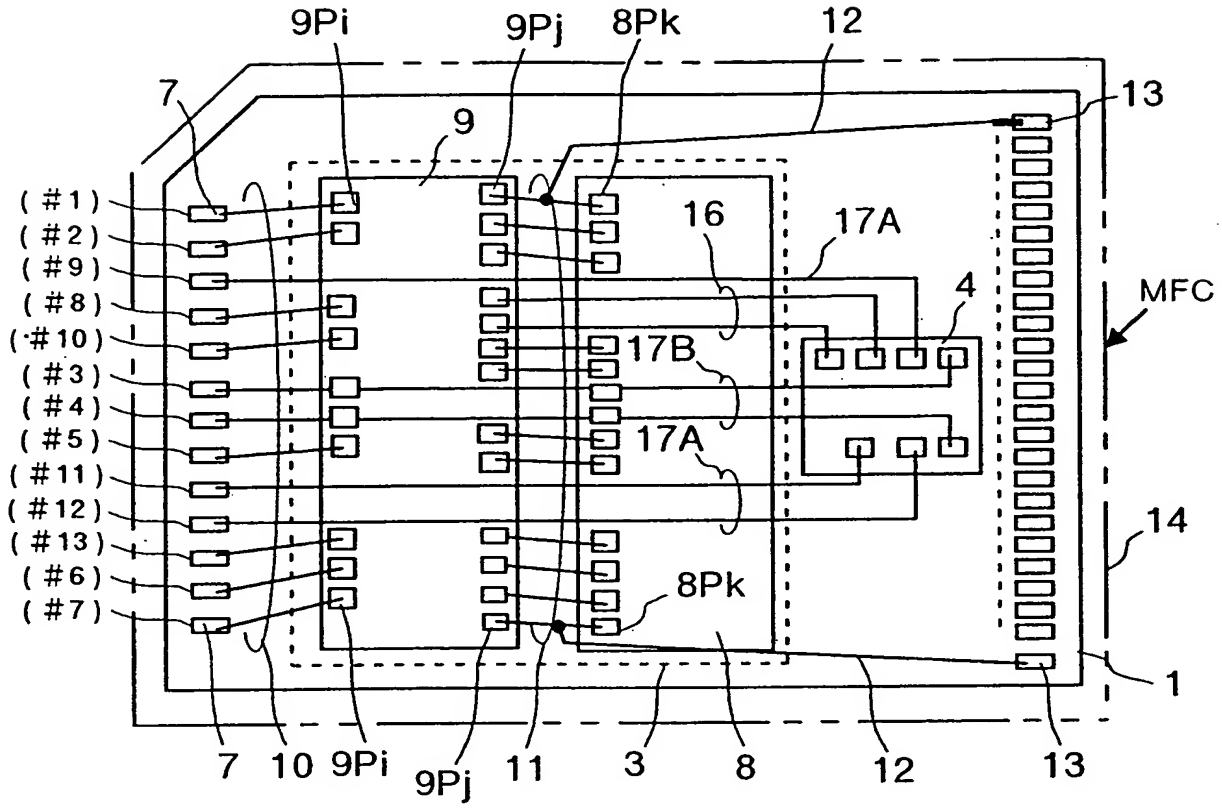

9Pi
9Pj
8Pk
12
7
9
13
(#1)
(#2)
(#9)
(#8)
(#10)
(#3)
(#4)
(#5)
(#11)
(#12)
(#13)
(#6)
(#7)
16
17A
4
MFC
17B
17A
14
8Pk
1
7
10
9Pi
9Pj
11
3
8
12
13

FIG. 4

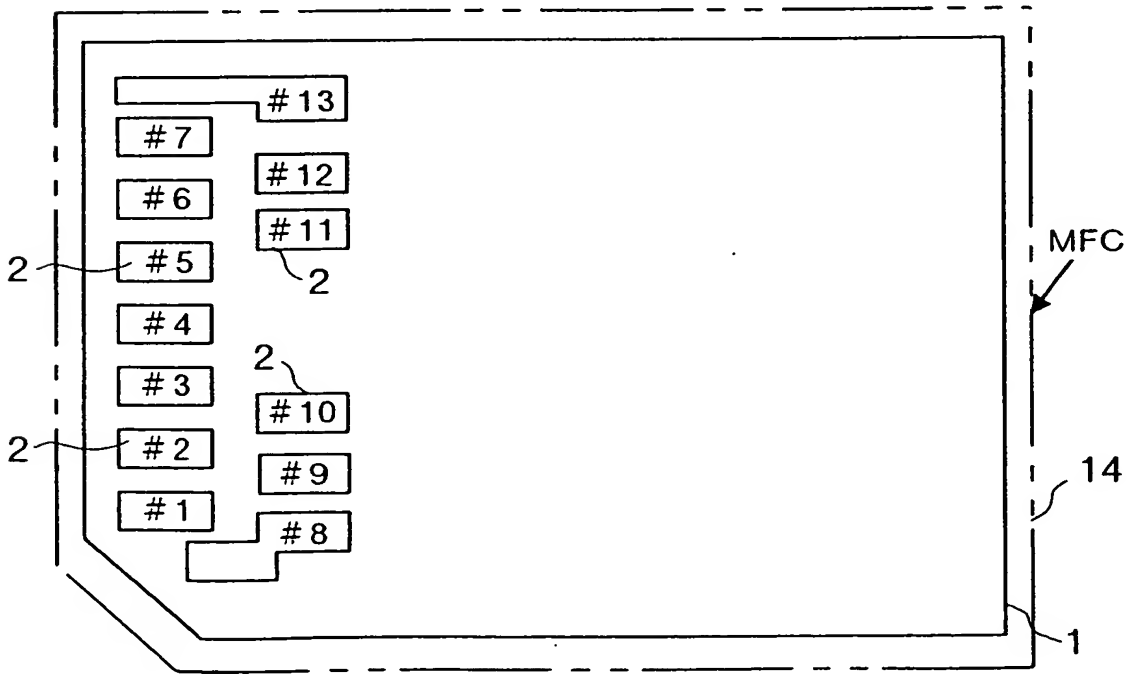

#13
#7
#12
#6
#11
2
#5
2
#4
2
#3
#10
2
#2
#9
#1
#8
MFC
14
1

FIG. 5

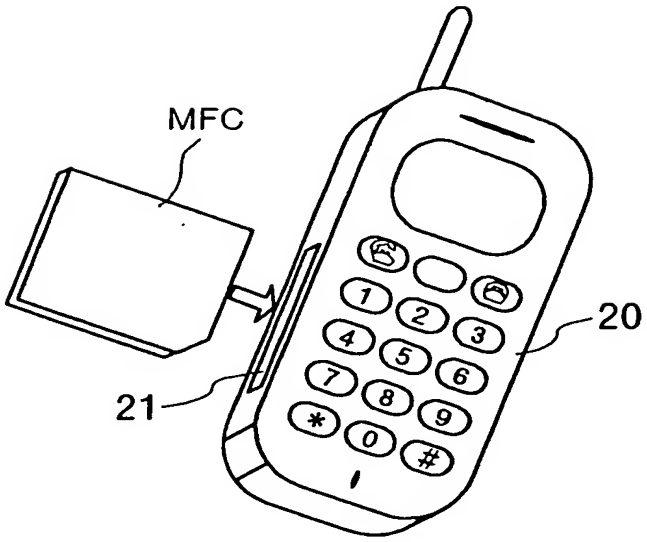

MFC
20
21

FIG. 9

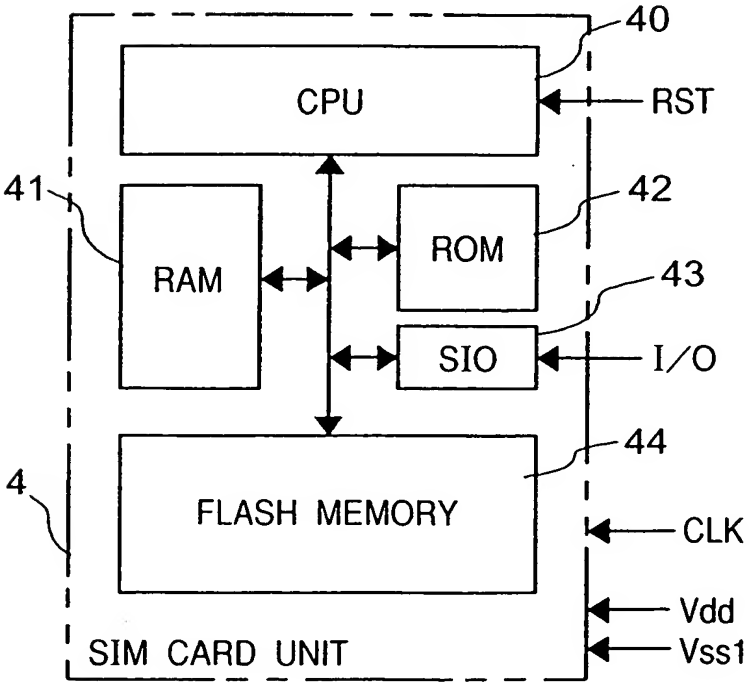

40
CPU
RST
41
RAM
ROM
42
43
SIO
I/O
44
4
FLASH MEMORY
CLK
Vdd
Vss1
SIM CARD UNIT

FIG. 6

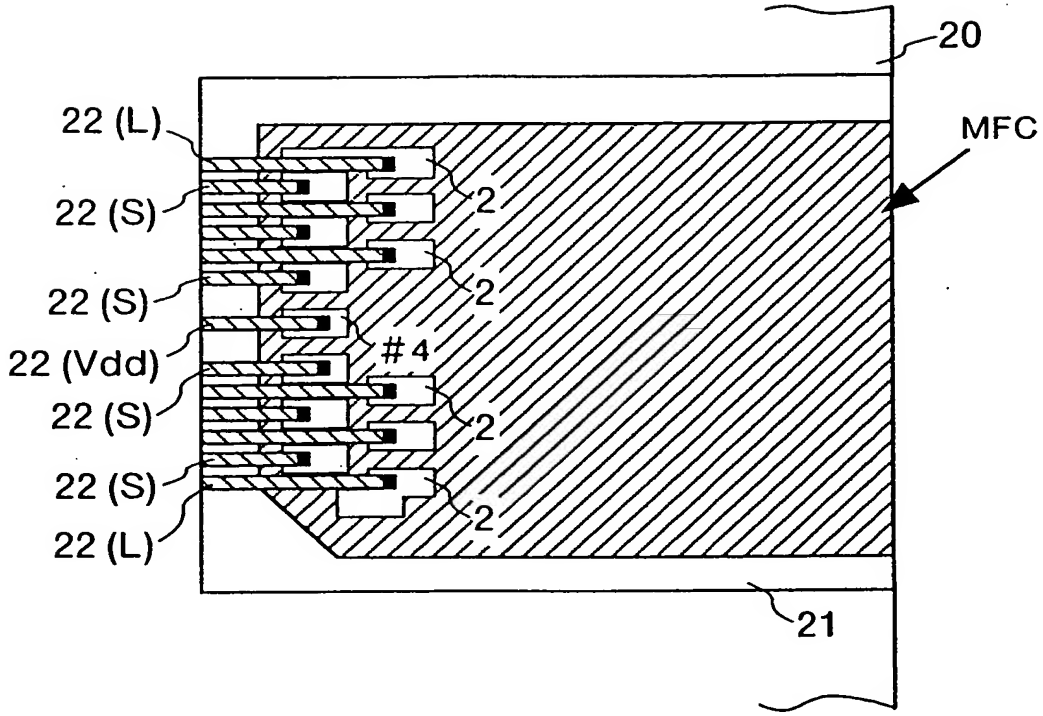

20
22 (L)
MFC
22 (S)
2
22 (S)
2
22 (Vdd)
#4
22 (S)
2
22 (S)
2
22 (L)
21

FIG. 7

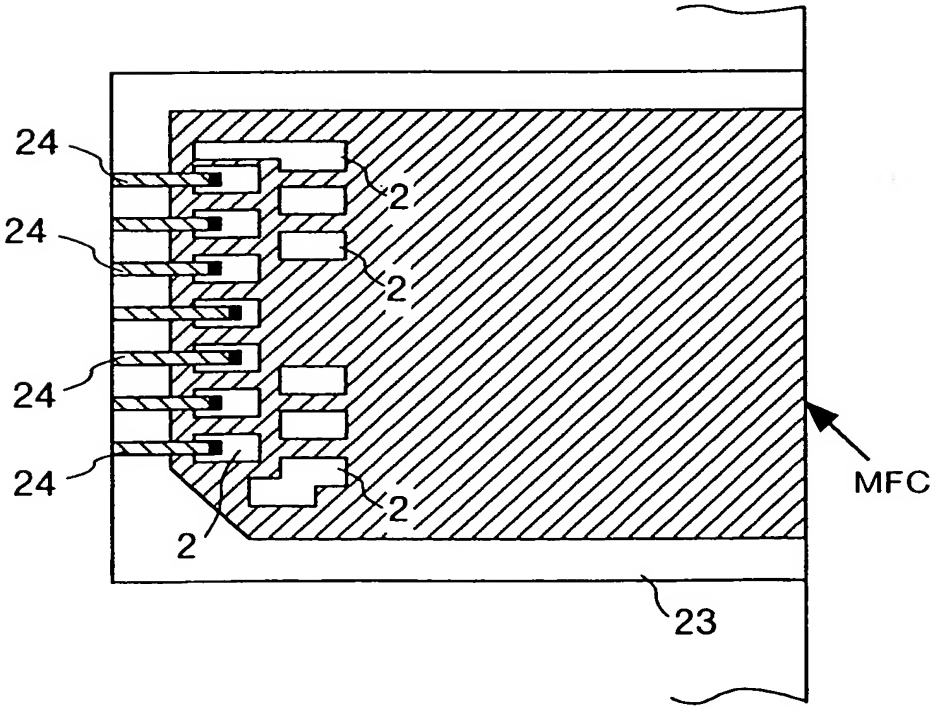
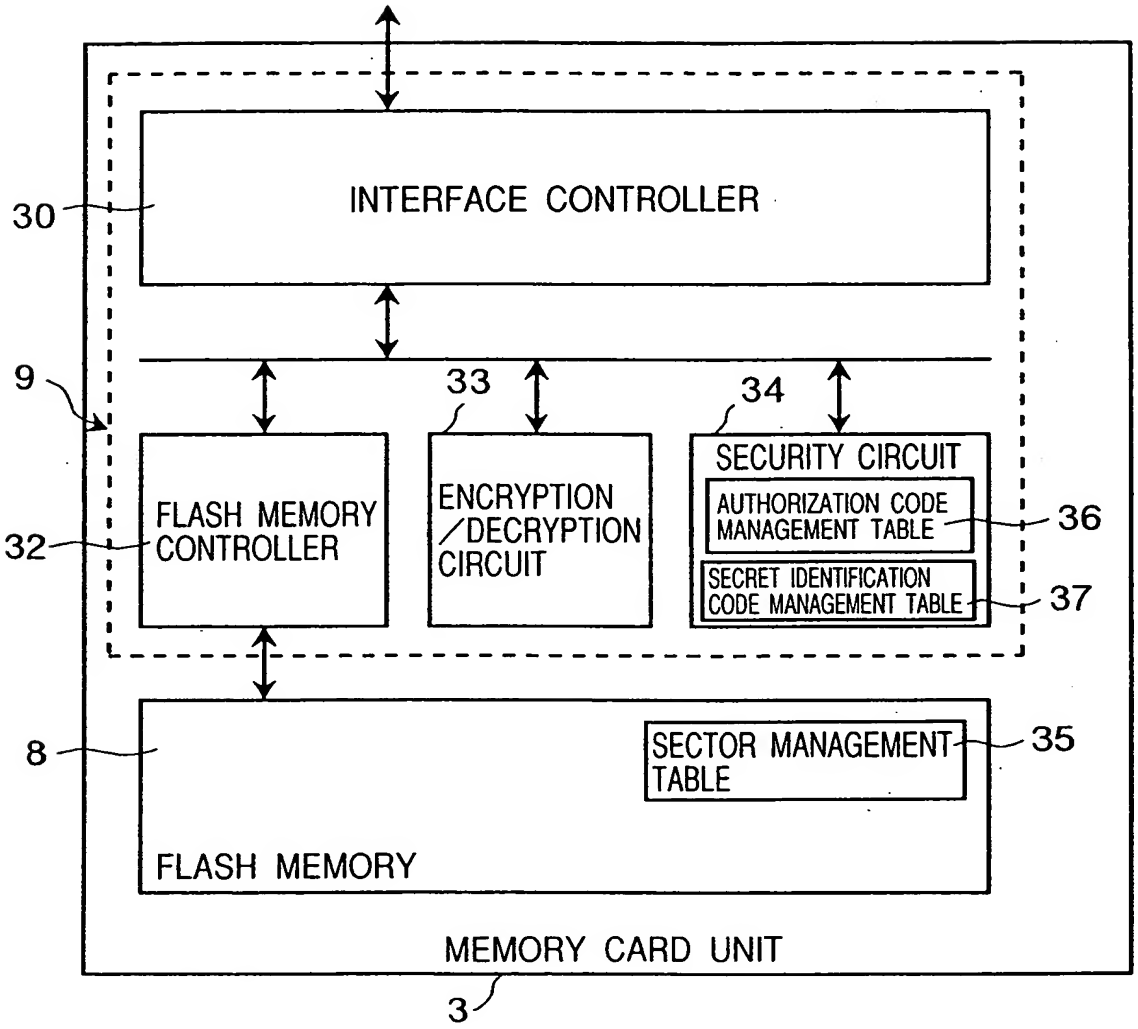

24
2
24
2
24
24
2
2
MFC
23

FIG. 8


INTERFACE CONTROLLER
30
9
33
34
FLASH MEMORY CONTROLLER
32
ENCRYPTION /DECRYPTION CIRCUIT
SECURITY CIRCUIT
AUTHORIZATION CODE MANAGEMENT TABLE
36
SECRET IDENTIFICATION CODE MANAGEMENT TABLE
37
8
SECTOR MANAGEMENT TABLE
35
FLASH MEMORY
MEMORY CARD UNIT
3

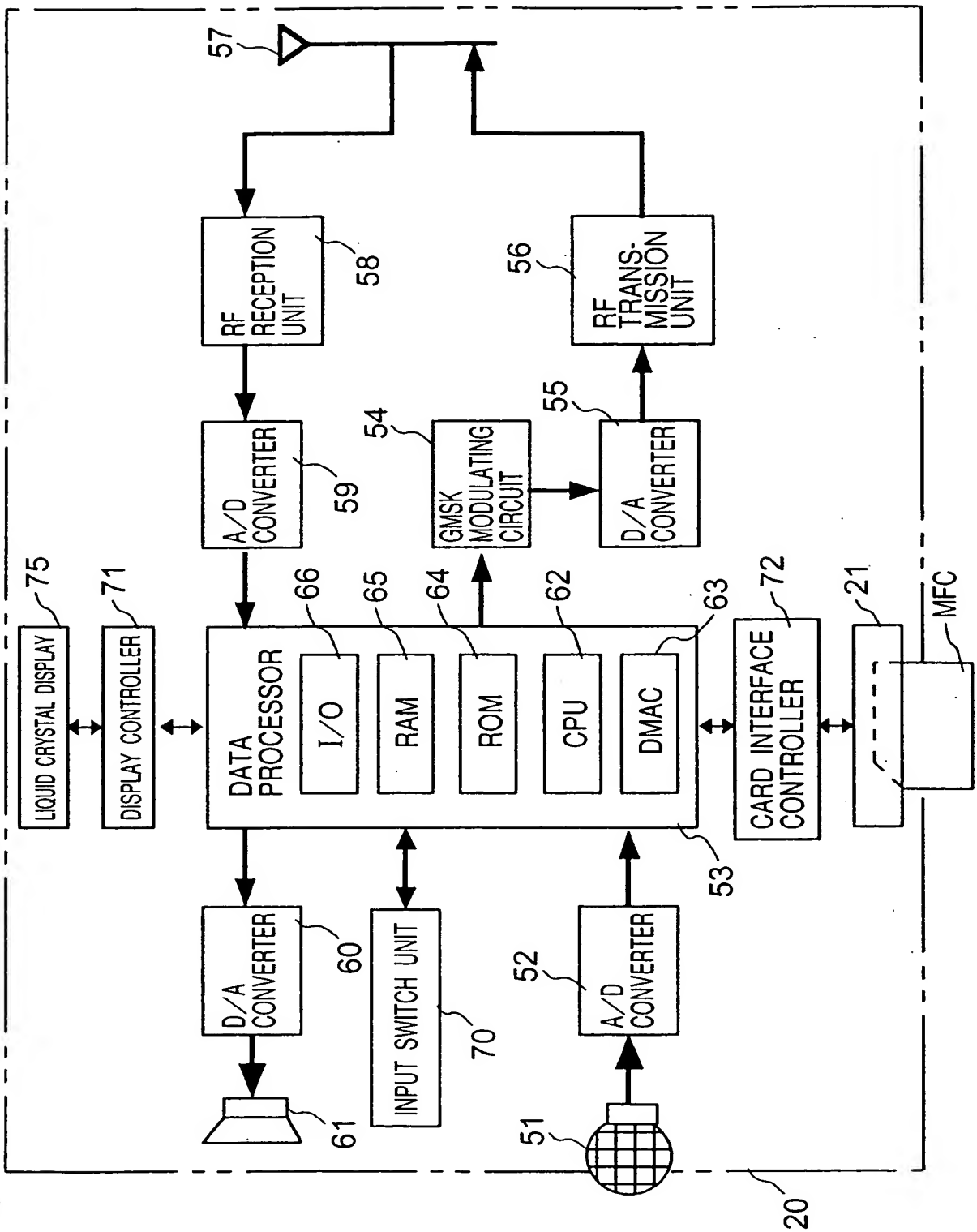

57
RF RECEPTION UNIT
58
RF TRANS-MISSION UNIT
56
A/D CONVERTER
59
GMSK MODULATING CIRCUIT
54
D/A CONVERTER
55
LIQUID CRYSTAL DISPLAY
75
DISPLAY CONTROLLER
71
DATA PROCESSOR
66
I/O
65
RAM
64
ROM
62
CPU
63
DMAC
53
CARD INTERFACE CONTROLLER
72
21
MFC
D/A CONVERTER
60
INPUT SWITCH UNIT
70
A/D CONVERTER
52
61
51
20

FIG. 10

FIG. 11

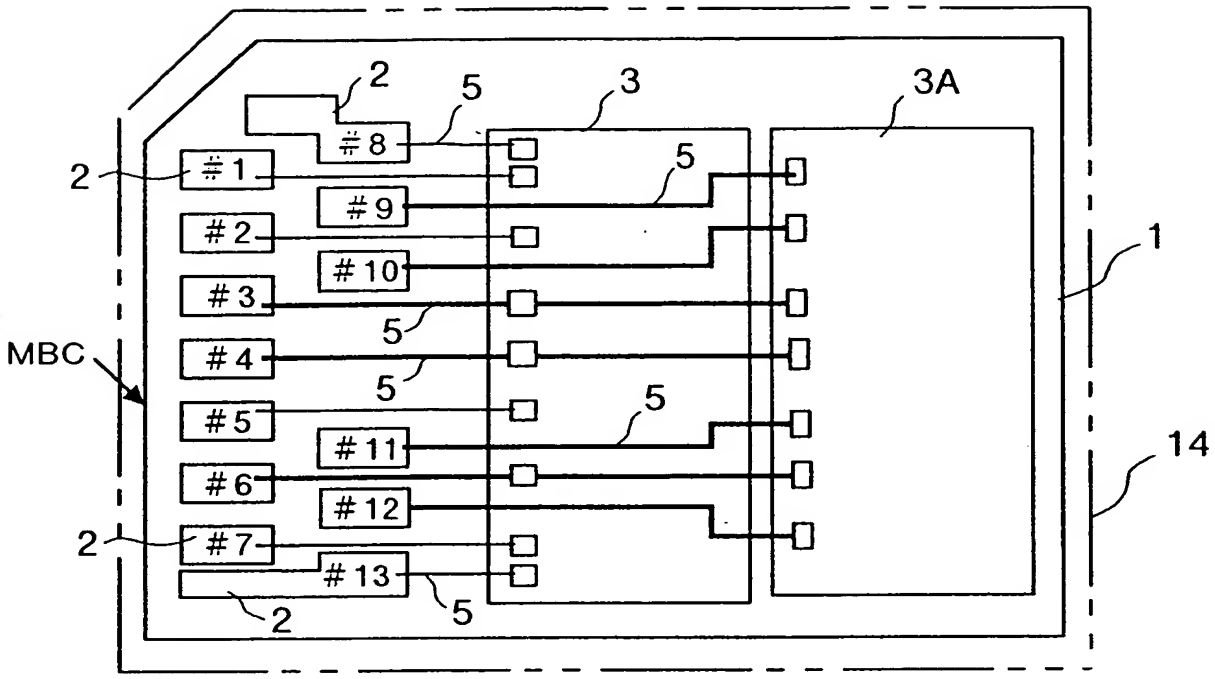
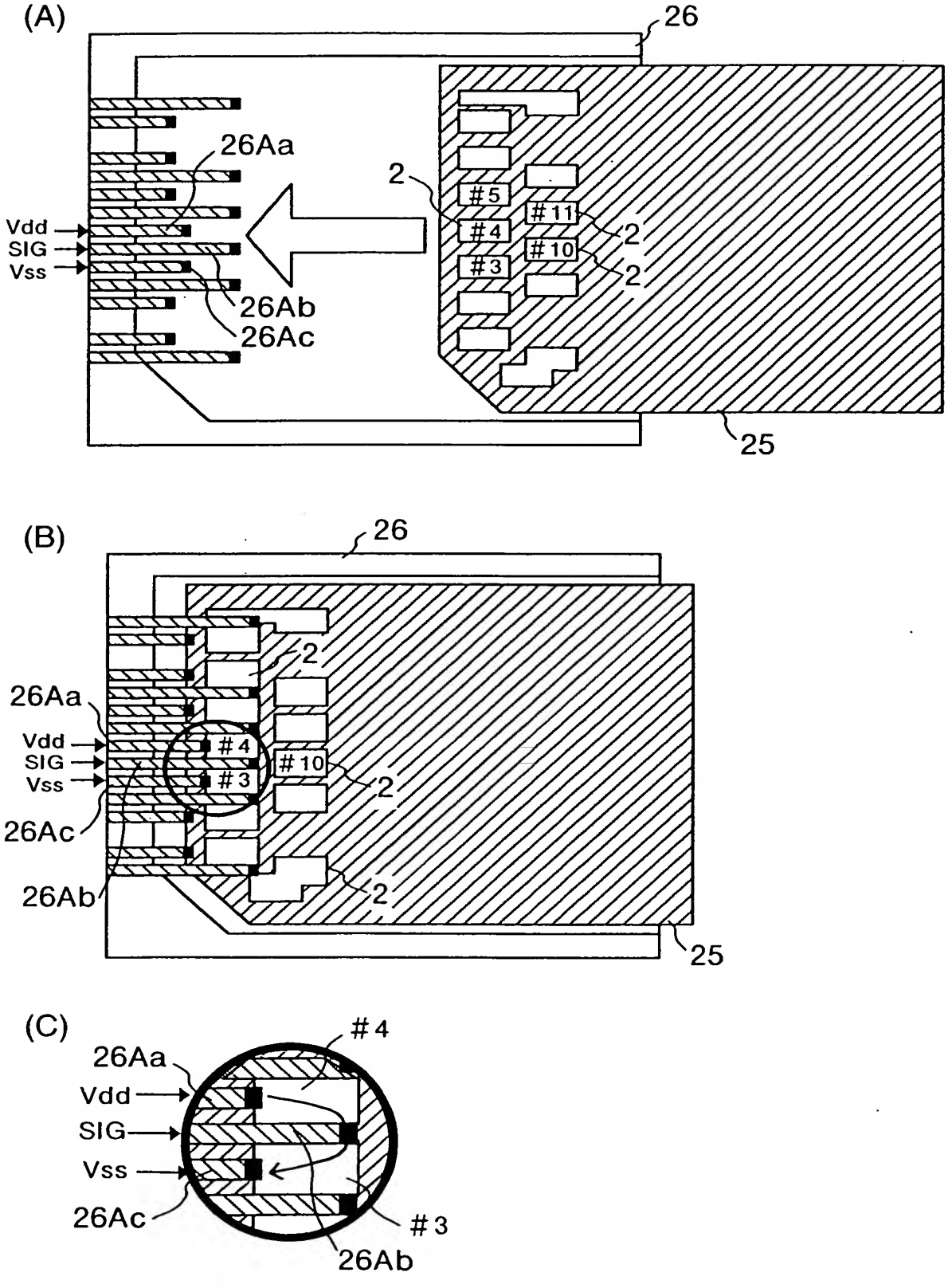


FIG. 12

pin#	7pin (1bit)	9pin (4bit)	13pin (A)	13pin (B)
1	(CS) NC	Data3	(CS-1) NC-1	Data3
2	(Din) CMD	CMD	(Din-1) CMD-1	CMD-1
3	Vss1	Vss1	Vss1	Vss1
4	Vdd	Vdd	Vdd	Vdd
5	CLK	CLK	CLK-1	CLK-1
6	Vss2	Vss2	Vss2	Vss2
7	(Dout) Data	Data0	(Dout-1) Data-1	Data0
8	-	Data2		Data2
9	-	-	(CS-2) NC-2	NC-2
10	-	-	(Din-2) CMD-2	• CMD-2
11	-	-	CLK-2	• CLK-2
12	-	-	(Dout-2) Data-2	• Data-2
13	-	Data1		Data1

FIG. 13


(A)
26
26Aa
2
#5
#11
#4
#10
#3
Vdd
SIG
Vss
26Ab
26Ac
25
(B)
26
2
26Aa
Vdd
SIG
Vss
#4
#3
#10
26Ac
26Ab
25
(C)
#4
26Aa
Vdd
SIG
Vss
26Ac
#3
26Ab